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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

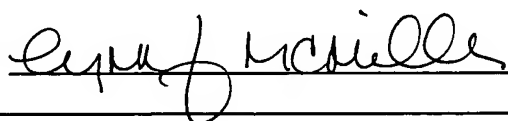
Applicant(s): Jie Xue, Zhiping Yang and Yida Zou
Serial No.: 10/792,101
For: TECHNIQUES FOR PROVIDING EMI SHIELDING WITHIN A
CIRCUIT BOARD COMPONENT
Filing Date: March 3, 2004
Examiner: Chandran, Biju Indira
Art Unit: 2835
Conf. No.: 4081

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: **MAIL STOP RCE**, Commissioner for Patents, PO Box 1450, Alexandria, Virginia 22313-1450 on:

Date: July 5, 2006

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Signature: 

MAIL STOP RCE
Commissioner for Patents
PO Box 1450
Alexandria, Virginia 22313-1450

AMENDMENT AFTER FINAL OFFICE ACTION AND ADVISORY ACTION

Sir:

In response to the Advisory Action mailed June 20, 2006 and the final Office Action mailed April 4, 2006, please amend this application as follows: